

Fig. 1

Operating conditions

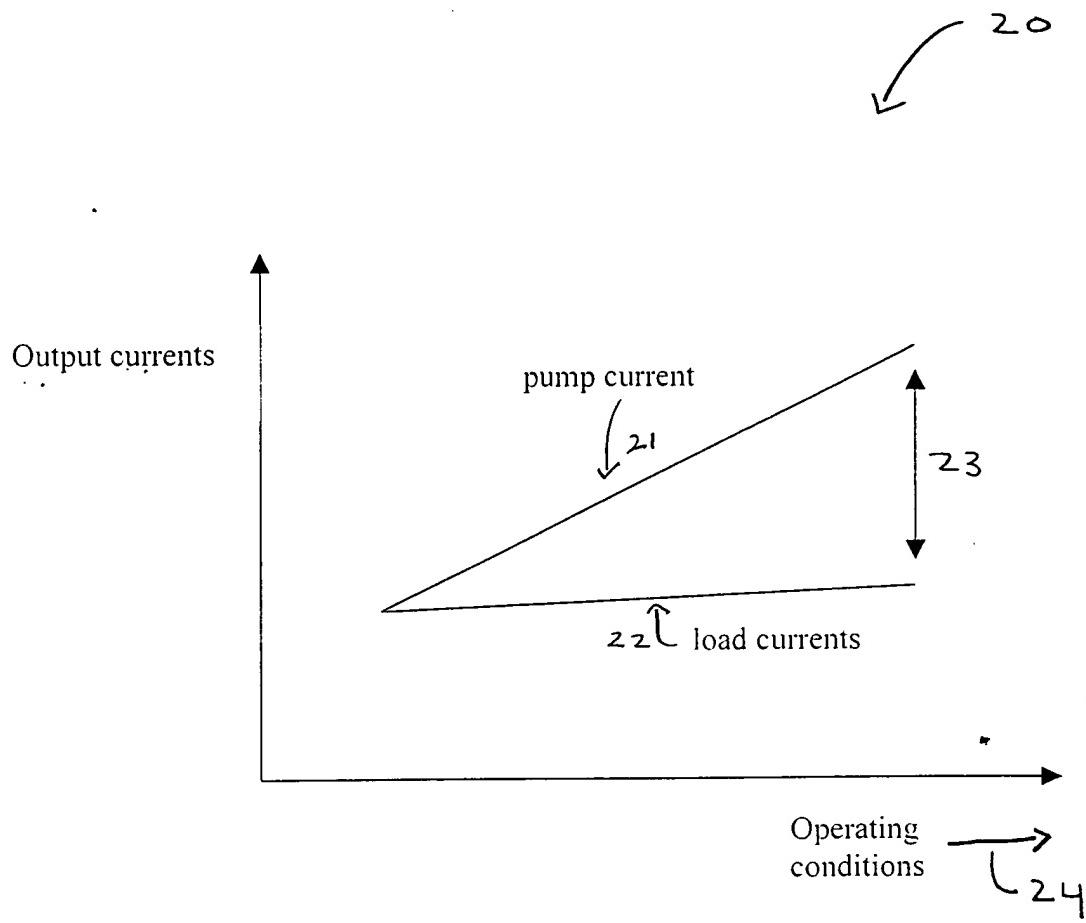


FIG. 2

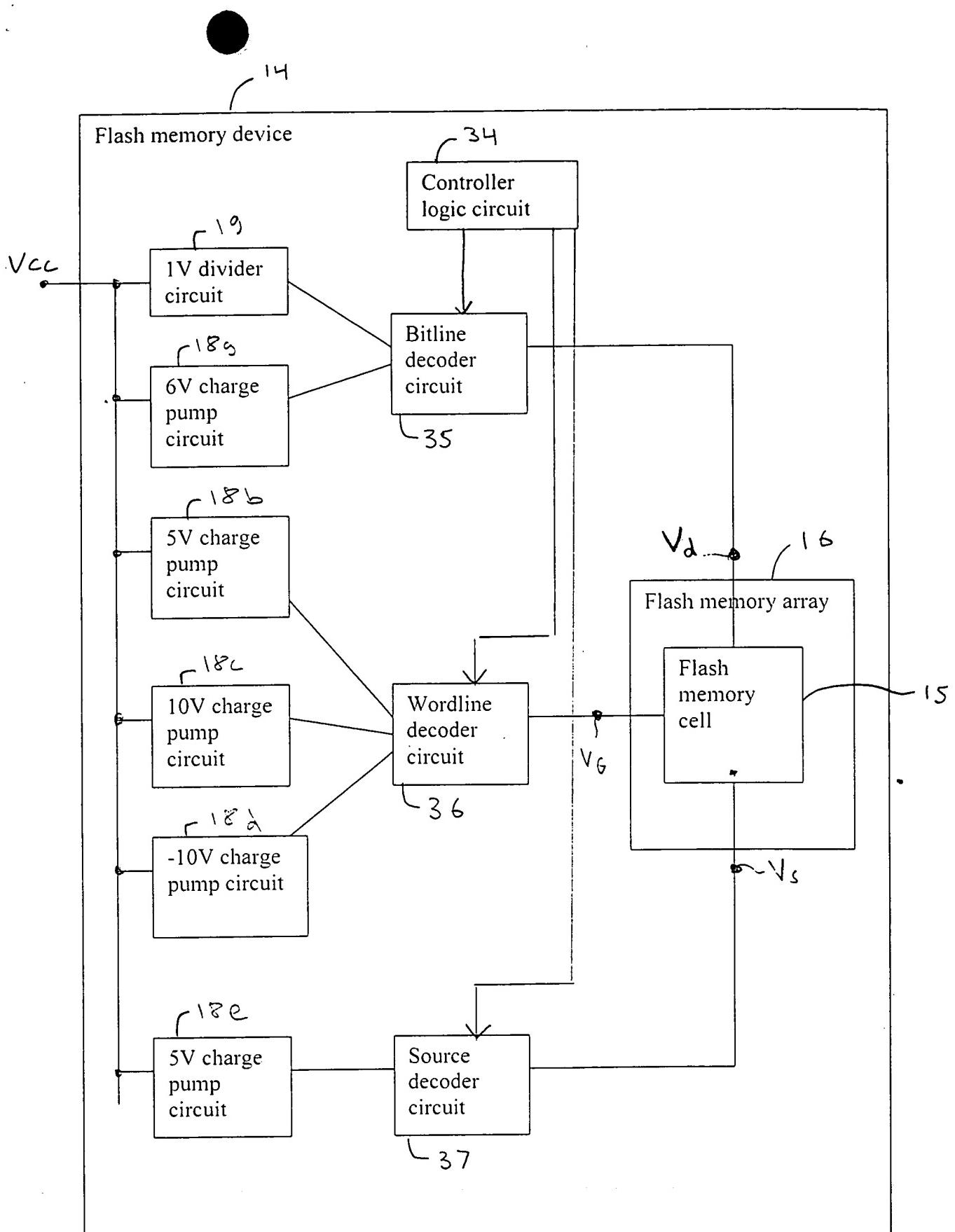


FIG. 3A

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Table : Voltages For Processing Flash Device

| | <u>Vg</u> | <u>Vd</u> | <u>Vs</u> |
|----------------|-----------|-----------|-----------|
| <u>Program</u> | 10 | 6 | 0 |
| <u>Erase</u> | -10 | 0 | float |
| <u>Read</u> | 5 | 1 | 0 |

Fig. 33

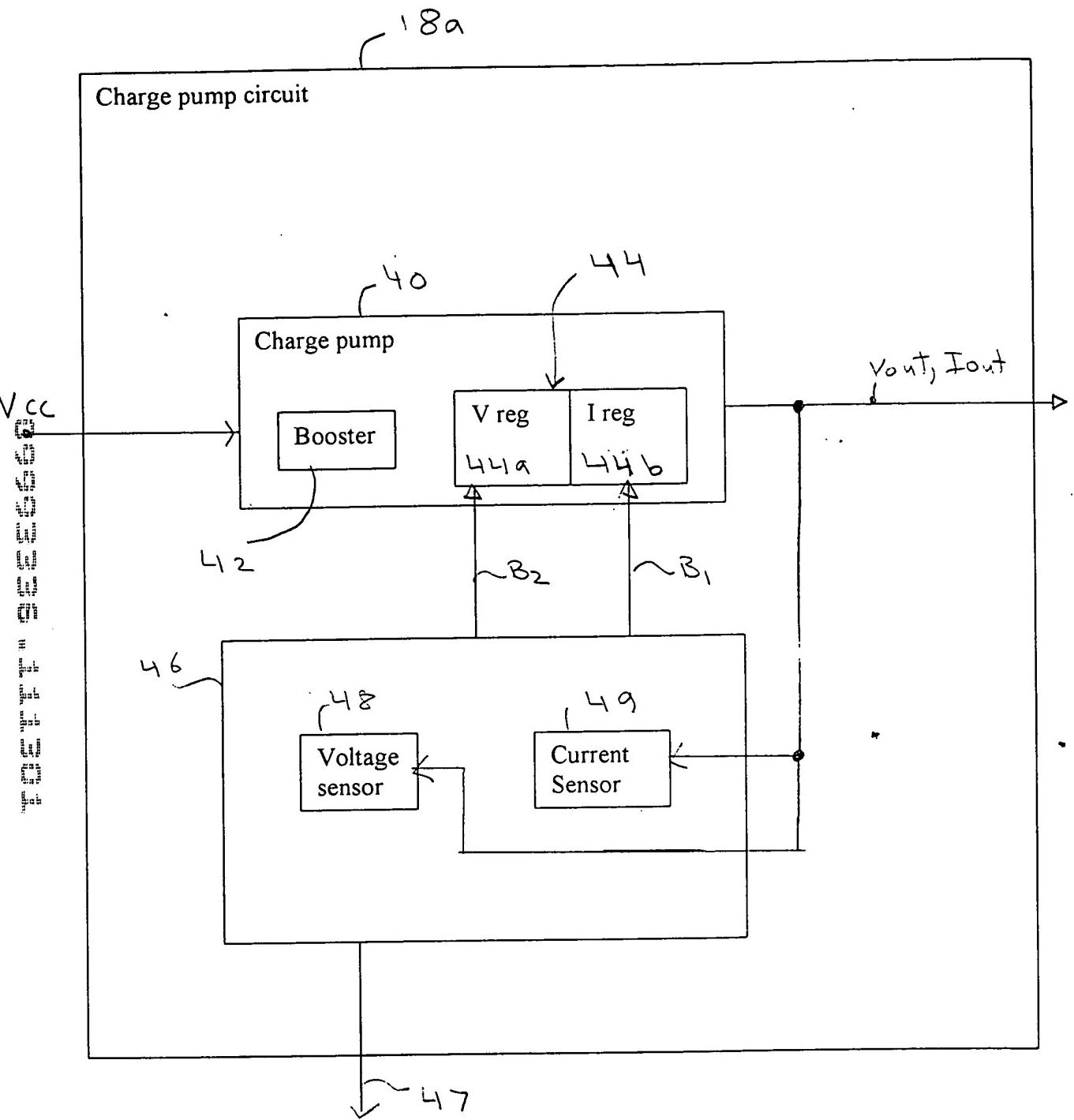


FIG. 4

Charge pump circuit

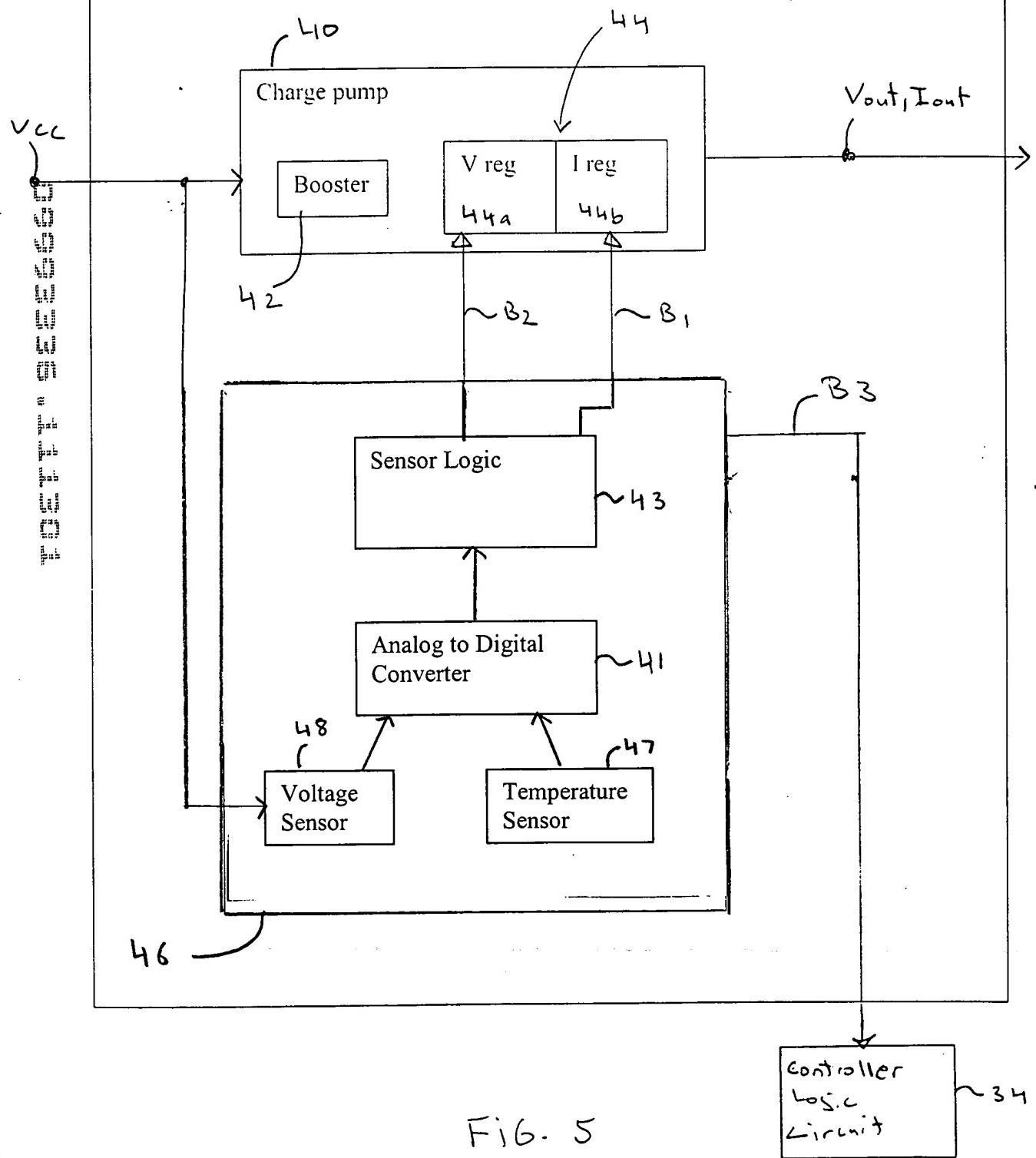


Fig. 5

Controller
Logic
Circuit

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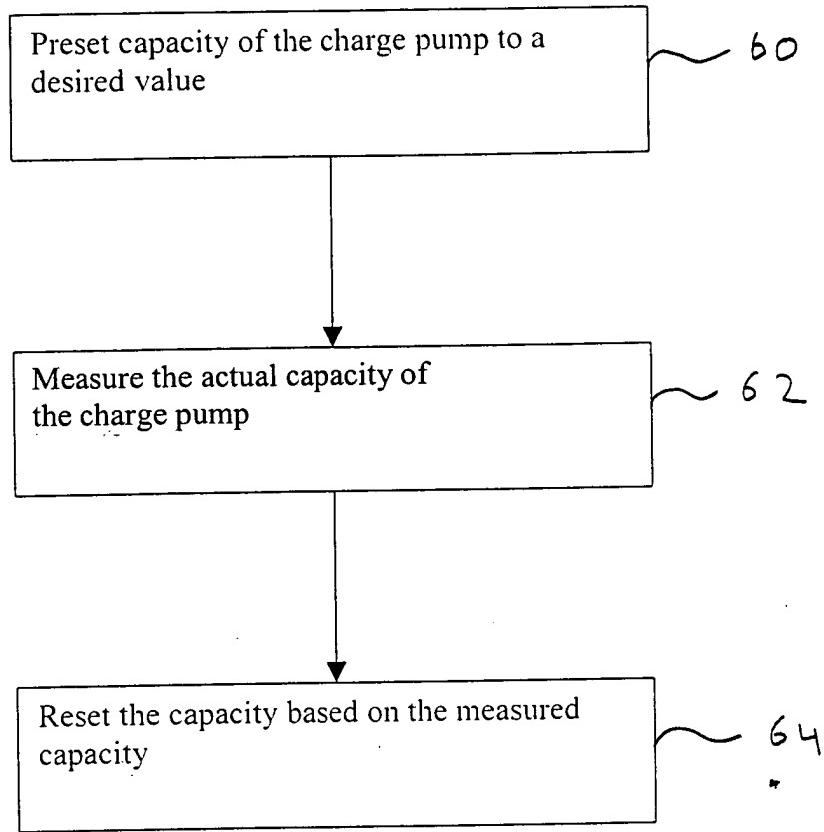


FIG. 6